

13142 U.S. PTO
021104

PATENT
W&B Docket No.: I0395US/LG
OC Docket No.: INFN/0056

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAIL STOP PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE UNDER 37 CFR 1.10

I hereby certify that this correspondence and the documents referred to as attached therein are being deposited on February 11, 2004 with the United States Postal Service in an envelope as "Express Mail Post Office to Addressee," mailing label No. EV335471975US addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 11, 2004

Date

Signature

Re:

Inventor(s): MARIO DI RONZA and YANNICK MARTELLONI

Title: MEMORY BUILT-IN SELF REPAIR (MBISR) CIRCUITS/DEVICES AND METHOD FOR REPAIRING
A MEMORY COMPRISING A MEMORY BUILT-IN SELF REPAIR (MBISR) STRUCTURE

Transmitted herewith is the patent application identified above, including:

- ☒ Specification, claims and abstract 22 Total Pages
☒ Drawings ☒ Formal ☐ Informal 8 Total Pages
☒ Declaration and Power of Attorney (Unsigned)
☒ Information Disclosure Statement with List
☐ Assignment of the Invention to
☐ Assignment Recordation Cover Sheet

19270 U.S. PTO
10/777025
021104

FEE CALCULATION

	NUMBER OF CLAIMS FILED	LESS NUMBER PAID BY BASIC FEE	NUMBER OF EXTRA CLAIMS (Not less than zero)	LARGE ENTITY FEE
Basic Fee				\$.00
Total Claims	33	- 20 = 13	X \$18 =	\$.00
Independent Claims	4	- 3 = 1	X \$86 =	\$.00
First Presentation of Multiple Dependent Claims				+ \$.00
Total Filing Fee Calculation				-0-
				\$.00

- ☐ The Commissioner is hereby authorized to charge \$ to Deposit Account No. A duplicate copy of this transmittal is enclosed.
- ☐ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. A duplicate copy of this transmittal is enclosed.
- ☒ Please address all future correspondence to:
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Respectfully submitted,

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